

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S73	409	(data (transmission or transfer\$4)) same ((supply\$3 or issu\$3 or generat\$3) near (interrupt\$4 signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 19:30
S72	2156	(data (transmission or transfer\$4)) same ((supply\$3 or issu\$3 or generat\$3) near3 interrupt)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 19:29
S69	22	(data (transmission or transfer\$4)) same ((supply\$3 or issu\$3 or generat\$3) near3 interrupt) near3 (busy or unavilable or available)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 19:17
S67	488	(data (transmission or transfer\$4)) same interrupt same buffer\$3 same (busy or unavilable or available)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 19:13
S62	13	(data (transmission or transferring)) same interrupt same (latch\$3 or hold\$3 or sav\$3) same (busy or (in use) or unavailable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 16:38
S56	224	interruption adj3 latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 16:30
S55	2	interruption state latch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 16:30
S47	10	((interrupt or interruption) near3 signal) near3 (sav\$3 or latch\$3) same (((CPU or processor or microprocessor or microcontroller)) near3 (busy or available))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 15:48
S45	377	((interrupt or interruption) near3 signal) near3 (sav\$3 or latch\$3) same ((CPU or processor or microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 15:48
S44	8	((interrupt or interruption) near3 signal) near3 (sav\$3 or latch\$3) same ((CPU or processor or microprocessor or microcontroller)) same busy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 15:21

S42	65	((interrupt or interruption) near3 signal) same latch\$3 same ((CPU or processor or microprocessor or microcontroller)) same busy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 15:11
S40	27	((interrupt or interruption) near3 signal) same latch\$3 same (sav\$3 or store\$3 or latch\$3) same buffer\$3 same ((CPU or processor or microprocessor or microcontroller)) same (idle or busy)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 14:49
S39	378	((interrupt or interruption) near3 signal) same latch\$3 same (sav\$3 or store\$3 or latch\$3) same buffer\$3 same ((CPU or processor or microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 14:13
S38	0	((interrupt or interruption) near3 signal) same latch\$3 same (sav\$3 or store\$3 or latch\$3) same buffer\$3 same ((CPU or processor or microprocessor or microcontroller) near busy)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 14:13
S37	590	((interrupt or interruption) near3 signal) same latch\$3 same (sav\$3 or store\$3 or latch\$3) same buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 14:12
S35	256	((cpu or processor or microprocessor or microcontroller) near3 (available or unavailable or busy)) same (interrupt signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/05/01 13:36